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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

MORI, et al.

Serial No.: 10/032,465

Filed: January 2, 2002

Allowed: May 6, 2004

Group Art Unit: 2819

Examiner: Daniel D. Chang

BATCH NO.: 6607

For: SEMICONDUCTOR INTEGRATED CIRCUIT

REQUEST FOR ACKNOWLEDGEMENT OF PRIORITY DOCUMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

May 20, 2004

Sir:

The undersigned respectfully requests that the Patent and Trademark Office acknowledge receipt of the priority document for the above-identified application.

The priority document were filed with the application January 2, 2002. Copies of the transmittal letter, the front cover of the priority document and the stamped postcard receipt are enclosed in support of this request.

In the event that any fees are due with respect to this paper, please charge our Deposit Account No. 01-2300.

Respectfully submitted,

ARENT FOX KINTNER PLOTKIN & KAHN, PLLC

Charles M. Marmelstein Attorney for Applicants Reg. No. 25.895

Atty. Docket No.: 108397-00058

1050 Connecticut Avenue, N.W. Suite 400 Washington, D. C. 20036-5339 Tel (202) 857-6000 Fax (202) 638-4810

CMM:mso

Enclosure: Copy of stamped postcard receipt



Receipt is hereby acknowledged of the papers filled as indicated in connection with the above-identified case.

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